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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.:

09/739,580

Filing Date:

12/15/00

For:

METHOD AND APPARATUS FOR

PRE-COMPUTING AND USING PLACEMENT COSTS WITHIN A PARTITIONED REGION FOR

MULTIPLE WIRING MODELS

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

**Assistant Commissioner for Patents** Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit for consideration in the aboveidentified application the documents listed on the attached Form PTO-1449. Copies of the documents are also submitted herewith. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

This Information Disclosure Statement is submitted within three months of the application filing date or before the mailing date of a first Office Action on the merits;

accordingly, no fee or separate requirements are required.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a

representation that: (i) a complete search has been made; (ii) additional information material to

the examination of this application does not exist; (iii) the information, protocols, results and the

like reported by third parties are accurate or enabling; or (iv) the above information constitutes

prior art to the subject invention.

In the unlikely event that the transmittal letter is separated from this document and the

Patent Office determines that an extension and/or other relief is required, applicant petitions for

any required relief including extensions of time and authorizes the Assistant Commissioner to

charge the cost of such petitions and/or other fees due in connection with the filing of this

document to **Deposit Account No. 50-1128** referencing SPLX.P0015. However, the Assistant

Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated:

March 1**2**, 2003

Respectfully submitted,

Registration No. 39,585

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**Application Number** 09/739,580 Substitute for form 1449A/PTO Filing Date 12/15/00 INFORMATION DISCLOSURE **First Named Inventor** Steven Teig STATEMENT BY APPLICANT Group Art Unit 2825 (use as many sheets as necessary) **Examiner Name** 3 Sheet 1 Attorney Docket Number SPLX.P0015

U.S. PATENTS RELATED TO THE PRESENT APPLICATIONS							
Examiner* Initials	A Number Kind Code			Name of Patentee or Applicant Date of Publication Relation to Present A MM-DD-YYYY			
	1.	6,516,455	B1	Teig, et al.	02-04-03		

				U.S. PATENT DOCUMENT	S	
Examiner* Initials	Cite No.1		ument d Code <sup>2</sup> known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	2.	2001/ 0003843	A1	Scepanovic et al.	06-14-01	
•	3.	4,615,011		Linsker	09-30-86	
	4.	5,097,422		Corbin, II et al.	03-17-92	
	5.	5,532,934		Rostoker	07-12-96	
	6.	5,566,078		Ding et al.	10-15-96	
	7.	5,578,840		Scepanovic et al.	11-26-96	
	8.	5,634,093		Ashida et al.	05-27-97	
	9.	5,636,125		Rostoker et al.	06-03-97	
	10.	5,650,653		Rostoker et al.	07-22-97	
	11.	5,742,086		Rostoker et al.	04-21-98	
	12.	5,757,656		Hershberger et al.	05-26-98	
	13.	5,777,360		Rostoker et al.	07-07-98	
	14.	5,811,863		Rostoker et al.	09-22-98	
	15.	5,822,214		Rostoker et al.	10-13-98	
	16.	5,859,449		Kobayashi et al.	01-12-99	
	17.	5,898,597		Scepanovic et al.	04-27-99	
	18.	5,914,887		Scepanovic et al.	06-22-99	
	19.	5973376		Rostoker et al.	10-26-99	
	20.	6,035,108		Kikuchi	03-07-00	
	21.	6,058,254		Scepanovic et al.	05-02-00	

Examiner	Date	
Signature	1	
Signature	Considered	

<sup>\*</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>8</sup> Applicant is to place a check mark here if English language Translation is attached.

09/739,580

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Sheet

Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATE

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EMENT BY APPLICANT	First Named Inventor	Steven Teig
	Group Art Unit	2825
as many sheets as necessary)	Examiner Name	

Attorney Docket Number

Application Number

Filing Date

3

			U.S. PATENT DOCUMENTS		
22.	6,068,662		Scepanovic et al.	05-30-00	
23.	6,123,736		Pavisic et al.	09-26-00	
24.	6,134,702		Scepanovic et al.	10-17-00	·
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27.	6,216,252	B1	Dangelo et al.	04-10-01	
28.	6,230,306	B1	Raspopovic et al.	05-08-01	
29.	6,247,167	B1	Raspopovic et al.	06-12-01	_
30.	6,249,902	B1	Igusa et al.	06-19-01	EC
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32.	6,262,487	B1	Igarashi et al.	07-17-01	b \$ C
33.	6,289,495	B1	Raspopovic et al.	09-11-01	20 11
34.	6,301,686	B1	Kikuchi et al.	10-09-01	2003 2003
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36.	6,401,234	B1	Alpert et al.	06-04-02	2.800
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39.	6,436,804	B2	Igarashi et al.	08-20-02	
40.	6,463,575	B1	Takahashi	10-08-02	

FOREIGN PATENT DOCUMENTS								
Examiner* Initials	Cite No.1	For Office <sup>3</sup>	eign Patent Do Number <sup>4</sup>	cument Kind Code (if known) <sup>5</sup>	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
	41.	JP	64-15947		Ouchi	01-19-89		

# NON PATENT LITERATURE DOCUMENTS

Examiner	Date	
Signature	Considered	

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09/739,580 Application Number Substitute for form 1449A/PTO Filing Date 12/15/00 INFORMATION DISCLOSURE **First Named Inventor** Steven Teig STATEMENT BY APPLICANT Group Art Unit 2825 (use as many sheets as necessary) **Examiner Name** 3 Sheet 3 Attorney Docket Number SPLX.P0015

		NON PATENT LITERATURE DOCUMENTS	
Examiner*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Т
	42.	A.H. Farrahi et al., Quality of EDA CAD Tools: Definitions, Metrics and Directions, Quality Electronic Design, 2000, Proceedings of the first International Symposium on March 2000, pp 395-405.	
	43.	C. Chiang, et al., Wirability of Knock-Knee Layouts with 45° Wires, IEEE Transactions on Circuits and Systems, Vol. 38, Issue 6, pp 613-624, June 1991.	
	44.	J. Su et al., Post-Route Optimization for Improved Yield Using Rubber-Band Wiring Model, 1997 International Conference on Computer-Aided Design, pp 700-706, November 1997.	
	45.	J. Vicente, RSR: A New Rectilinear Steiner Minimum Tree Approximation for FPGA Placement and Global Routing, Proceedings of the 24 <sup>th</sup> Euro Micro Conference, pp 192-195, August 1998.	
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	47.	M. Alexander et al., Performance-Oriented Placement and Routing for field-programmable gate arrays, Proceedings of the European Design Automation Conference, pages 80-85, 1995.	
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	49.	M. Igarashi et al., A Diagonal-Interconnect Architecture and Its Application to RISC Core Design, 2002 IEEE Solid-State Circuits Conference, pp 210-460, February 2002.	
	50.	P. Dood, et al. A Two-Dimensional Topological Compactor with Octagonal Geometry, 28 <sup>th</sup> ACM/IEEE Design Automation Conference, pp 727-731, July 1991.	
	51.	P. Parikh, et al., Congestion Driven Quadratic Placement, Proceedings of Design Automation Conference, 1998, pp 275-278.	
	52.	R. Putatunda et al., VITAL: Fully Automatic Placement Strategies for Very Large Semicustom Designs, Proceedings of the International Conference on Computer Design: VLSI in Computers and Processors, pp 434-439 October 1988.	
	53.	S. Dutt, et al., Probability-Based Approaches to VLSI Circuit Partitioning, IEEE Trans. on Computer-Aided Design of IC's and Systems, Vol. 19, No. 5, May 2000, pp 534-549.	
	54.	Y. Sekiyama et al., Timing-Oriented Routers for PCB Layout Design of High-Performance Computers, International Conference on Computer Aided Design, pp 332-335, November 1991.	

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PTO/SB/21 (6-98) (modified)
Approved for use through 09/30/00. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

09/739,580

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FORM	

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

 Filing Date
 12/15/00

 First Named Inventor
 Steven Teig

 Group Art Unit
 2825

 Examiner Name
 <none yet>

 Attorney Docket Number
 SPLX.P0015

**Application Number** 

	Enclosures (Check all that apply)							
Fee Transmittal Form	Assignment Papers (for an application)							
Fee Attached	Drawing(s)		Communication to Board of s and Interferences					
Amendment/Reply	Licensing-related Papers	Appeal	Communication to Group					
After Final	Petition	Proprie	tary Information					
Affidavits/declarations(s)	Petition to Convert a Provisional Application	Status	Letter					
Preliminary Amendment								
Extension of Time Request	Power of Attorney, Revocation Change of Correspondence Address	Other E	Enclosures (Listed below):					
Request to Rescind Previous Non-Publication Request	Terminal Disclaimer		erences ·					
Information Disclosure Statement and PTO-1449	Request for Refund	TECH						
Certified Copy of Priority Documents	Remarks:		MAR					
Response to Missing Parts/ Incomplete Application			ECEI IR 20 JGY CI					
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Firm <i>Or</i> Individual name Mani	deli, Reg. No. 39,585 of Stattler Joh	ansen & Ad	eli LLP					
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